

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	10/706,931	NAKATA, KENICHI	
	Examiner	Art Unit	Page 1 of 1
	Harry Behm	2838	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,646,426	11-2003	Terashi, Hiroto	323/285
*	B	US-2002/0135345	09-2002	Terashi, Hiroto	323/283
*	C	US-6,865,682	03-2005	Talbot et al.	713/300
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Talbot, Gerald; Microprocessor module with integrated voltage regulators; December 28, 2000; WO 2000/79370.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.